

WHAT IS CLAIMED IS:

1. An amplifier apparatus, comprising:

a signal amplifier including a signal input for receiving a signal to be amplified, a signal output for providing an amplified signal, and at least two control connections for setting a gain of the signal amplifier;

a control block including a control input for receiving a control signal, and including at least two output connections which are connected to the at least two control connections, the control block responsive to the control signal for producing at the output connections respective control block output signals; and

a trimming unit coupled to the control block for trimming the control block output signals relative to one another.

2. The apparatus of Claim 1, wherein the control connections form a difference signal input of the signal amplifier.

3. The apparatus of Claim 2, wherein the control input includes only a single node, the control signal is a single node signal, and the control block includes a converter for converting the single node signal into a difference signal.

4. The apparatus of Claim 3, wherein the converter includes an adjustable component, the trimming unit for adjusting the adjustable component.

5. The apparatus of Claim 4, wherein the adjustable component is a resistance network.

6. The apparatus of Claim 5, wherein each of the control block output signals has a quantifiable characteristic, and wherein the trimming unit is for adjusting the resistance network until a value of the quantifiable characteristic of one of the control block output signals is at least as high as a value of the quantifiable characteristic of the other of the control block output signals.

7. The apparatus of Claim 6, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

8. The apparatus of Claim 5, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

9. The apparatus of Claim 4, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

10. The apparatus of Claim 3, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

11. The apparatus of Claim 2, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

12. The apparatus of Claim 1, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

13. The apparatus of Claim 1, wherein the control input includes only a single node, the control signal is a single node signal, and the control block includes a converter for converting the single node signal into a difference signal.

14. The apparatus of Claim 13, wherein the converter includes an adjustable component, the trimming unit for adjusting the adjustable component.

15. The apparatus of Claim 14, wherein the adjustable component is a resistance network.

16. The apparatus of Claim 15, wherein each of the control block output signals has a quantifiable characteristic, and wherein the trimming unit is for adjusting the resistance network until a value of the quantifiable characteristic of one of the control block output signals is at least as high as a value of the quantifiable characteristic of the other of the control block output signals.

17. The apparatus of Claim 16, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

18. The apparatus of Claim 15, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

19. The apparatus of Claim 14, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

20. The apparatus of Claim 13, wherein the control block is for performing automatic gain control with respect to the gain of the signal amplifier.

21. The apparatus of Claim 1, wherein the signal amplifier is a radio frequency signal amplifier.

22. A transmitter apparatus, comprising:

a signal amplifier including a signal input for receiving a signal to be amplified, a signal output for providing an amplified signal, and at least two control connections for setting a gain of the signal amplifier;

a control block including a control input for receiving a control signal, and including at least two output connections which are connected to the at least two control connections, the control block responsive to the control signal for producing at the output connections respective control block output signals;

a trimming unit coupled to the control block for trimming the control block output signals relative to one another; and

a modulator for modulation of a data signal onto a carrier signal, the modulator having an output coupled to the signal input of the signal amplifier.

23. The apparatus of Claim 22, provided as a Universal Mobile Telecommunications Standard (UMTS) radio transmitter apparatus.